

ANALOG WAY MIDRA

Module: INPUT_VIDEO

Crestron 2-series & 3-series

Date: **December 10, 2013**
Driver version: **V1.00**
Tested with: **Midra Firmware V01.00.12**

GENERAL

This is an optional module for controlling Midra series switchers. This module allows you to manage Midra switcher inputs:

- Input validity
- Freeze management
- Input plugs management
- Input auto-Setting
- Input auto-Center
- Input misc. status

CONNECTION

This module must be connected to Midra_Main module.

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Control

Inter_connect_module

From_Module_Main	String_in	To be connected to the Midra_Main module
Refresh_All	Digital_in	Pulse for module initialization
To_Module_Main	String_out	To be connected to the Midra_Main module
Message_Txt	String_out	Midra_In module text status. To be connected to the Midra_Main module
Refresh_In_Progress_FB	Digital_out	Module refresh in progress
Next_Module_Refresh_OS	Digital_out	To be connected to next module for daisy chain initialization

General

X is the input number (1=>10)

InputX_Available_FB	Digital_out	1 if Input X is valid
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Check_Input

X is the input number (1=>10)

Check_InputX_Infos_PB	Digital_in	Pulse to refresh Input X status
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InputX

X is the input number (1=>10)

Y is the plug number (0=>3)

InputX_Freeze_PB	Digital_in	Pulse to freeze/unfreeze Input X
InputX_Auto_Setting_PB	Digital_in	Pulse to start auto-setting for Input X
InputX_Auto_Center_PB	Digital_in	Pulse to start auto-center for Input X
InputX_Plug_Select	Ana_in	Input X Plug selection (see table below for values)
InputX_Freeze_FB	Digital_out	Freeze status for Input X
InputX_Auto_Setting_FB	Digital_out	Auto-setting status for Input X
InputX_Auto_Center_FB	Digital_out	Auto-center status for Input X
InputX_Auto_Center_Progress_FB	Ana_out	Auto-center progression for Input X
InputX_Auto_Setting_Progress_FB	Ana_out	Auto-setting progression for Input X
InputX_Signal_Detected_FB	Digital_out	Input X signal detected

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InputX_HDCP_FB	Digital_out	Input X HDCP status
InputX_Signal_Format_FB	Ana_out	Signal format for Input X (see table below for values)
InputX_Signal_Format_Name	String_out	Signal format name for Input X (see table below for possible values)
InputX_Active_Plug_FB	Ana_out	Input X selected plug (see table below for values)
InputX_PlugY_Available_FB	Digital_out	Plug Y validity status for Input X
InputX_Signal_Width_FB	Ana_out	Input X Signal width (pixel)
InputX_Signal_Height_FB	Ana_out	Input X Signal height (pixel)

Values for InputX_Plug_Select and InputX_Active_Plug_FB

0	Plug of analog type (HD15 socket)
1	Plug of DVI type (Single or dual link)
2	Plug of SDI type
3	Plug of HDMI type

Values for InputX_Signal_Format_FB

0	NONE
1	INVALID
2	UNKNOWN
3	SDTV NTSC
4	SDTV PAL
5	SDTV SECAM
6	SDTV 480i
7	SDTV 576i
8	EDTV 480p
9	EDTV 576p
10	HDTV 720p
11	HDTV 1035i
12	HDTV 1080i
13	HDTV 1080p
14	HDTV 2048x1080 Cinema
15	CEA861 720x240p
16	CEA861 720x288p
17	CPU 640x350
18	CPU 640x400
19	CPU 720x400
20	CPU VGA
21	CPU WVGA 5/3
22	CPU WVGA
23	CPU SVGA
24	CPU 1280x600
25	CPU 720p RGB

26	CPU XGA
27	CPU WXGA
28	CPU SWXGA
29	CPU 1366x768
30	CPU 800p RGB
31	CPU SWXGA+
32	CPU 1152x864
33	CPU 900p RGB
34	CPU 1600x900
35	CPU 960p RGB
36	CPU SXGA
37	CPU SXGA3
38	CPU SXGA+
39	CPU WSXGA+
40	CPU 1080p RGB
41	CPU 2K
42	CPU QWXGA
43	CPU UXGA
44	CPU WUXGA
45	CPU 1792x1344
46	CPU 1856x1392
47	CPU 1920x1440
48	CPU WQHD
49	CPU QXGA
50	CPU WQXGA
51	CPU CVT Timing
52	CPU GTF Timing with 5/4 aspect ratio
53	CPU GTF Timing with 4/3 aspect ratio
54	CPU GTF Timing with 16/10 aspect ratio
55	CPU GTF Timing with 15/9 aspect ratio
56	CPU GTF Timing with 16/9 aspect ratio
45	CPU 1792x1344
46	CPU 1856x1392
47	CPU 1920x1440
48	CPU WQHD
49	CPU QXGA
50	CPU WQXGA
51	CPU CVT Timing
52	CPU GTF Timing with 5/4 aspect ratio
53	CPU GTF Timing with 4/3 aspect ratio
54	CPU GTF Timing with 16/10 aspect ratio
55	CPU GTF Timing with 15/9 aspect ratio
56	CPU GTF Timing with 16/9 aspect ratio

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